



Protectli Appliance Protectli Vault V1210 2 Port - Intel® N5105

July 4, 2024



Specifications

Model	V1210		
Description	2x 2.5G Network Port Fanless Appliance		
Processor	Intel® N5105 (64 Bit, 2.0GHz, Turbo 2.9GHz, 4M L3 Cache)		
Processor Cores	4		
Processor Threads	4		
Intel AES-NI	Supported		
Virtualization	Intel Vt-x, Vt-d		
Network	2x Intel I226-V 2.5G Ethernet, RJ-45		
Video / Graphics	Intel UHD Graphics, 1x HDMI 1.4		
Audio	Audio over HDMI		
Memory	1x 4GB LPDDR4-2933, Soldered		
Storage	1x M.2 2280 NVMe, 1x 32G eMMC on board		
Optional Storage	None		
External I/O	2x RJ-45 Ethernet		
	4x USB 3.2 Gen 2 Type A		
	1x USB Type C Console		
	1x HDMI		
	Reset Button (Recessed), GPIO		
	6x WiFi/LTE Antenna Mounting Holes		
	1x 12V DC Power Jack, Threaded		
Internal I/O	1x M.2 2280 M-Key PCIe 3.0 x1 (NVMe)		
	1x M.2 2230 E-Key PCle 3.0 x1 for WiFi		
	1x M.2 3052 B-Key USB 3.2 Gen 2 (LTE)		
	1x CMOS Reset (3 pin)		
BIOS	AMI or coreboot		
Indicators	1x LED Power Button (Blue), 1x LED Power Indicator (Green), 1x LED Disk Activity Indicator (Red)		
Power	Input 12V DC, 1x DC Power Jack, Threaded connector		
Power Usage	Max 24W		
Chassis	Fanless, Aluminum, Gray		
Chassis Dimensions	4.5 x 4.5 x 2 in, 115 x 115 x 50 mm		
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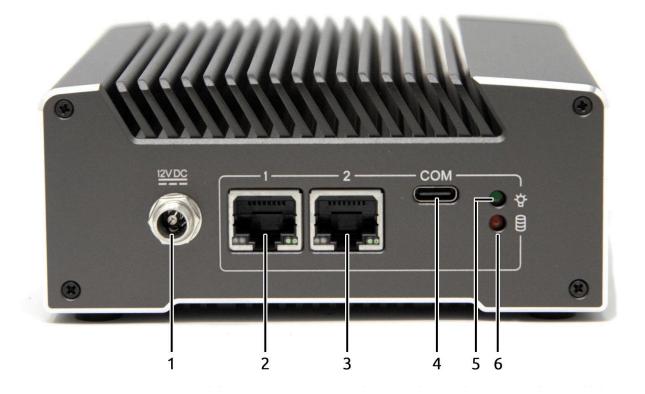


Mounting Options	Desktop, Optional VESA Bracket, Optional 1RU Rack Mount
Weight	1 lb, 0.45 kg
Shipping Weight	3 lbs 4 oz, 1.47 kg
Operating Temperature	+14° - +122° F, -10° - +50° C
Operating Humidity	0 – 95% relative humidity, non-condensing
Approvals	UL (Power Supply), FCC Part 15 Class B, CE, RoHS
Country of Origin	Made in China, Assembled in USA, Canada, or Germany
Optional Connectivity	1x WiFI, 1x LTE



System Features

Front Features



ltem #	Object	Description	
1	Power Supply Connector	12V DC threaded barrel connector (2.5mm x 5mm) for the 48W external power supply. Positive rail is the tip, negative is sleeve.	
2	Ethernet Port 1	The first 100/1000/2500 Mbps Intel® i226-V ethernet port.	
3	Ethernet Port 2	The second 100/1000/2500 Mbps Intel® i226-V ethernet port.	
4	Serial Console Port	RS232 serial communications via FTDI FT232RQ UART, exposed through USB 2.0 Type C connector. Default port settings: • 115200 baud	



		 No parity 8 databits 1 stopbit 	
5	Power Indicator LED	This LED will stay solid green when the device is powered on.	
6	HDD Activity LED	This red LED will light up when data activity is detected over the NVMe interface.	

Rear Features



ltem #	Object	Description
1	Reset Button (Recessed)	A momentary switch exposed via GPIO. This is not an ACPI reset button, but a general purpose button that may be programmed in the guest OS.
2	Power Button	Pressing the Power Button will power the unit on and illuminate with a blue LED. In OSes configured to handle ACPI signals, pressing the power button initiates a shutdown.



		Pressing and holding the Power Button for 5 seconds will force the unit to power off.	
3	Four USB3 Connectors	USB 3.2 Gen 2 Type-A connectors.	
4 HDMI Connector		Video and audio output via HDMI 1.4.	

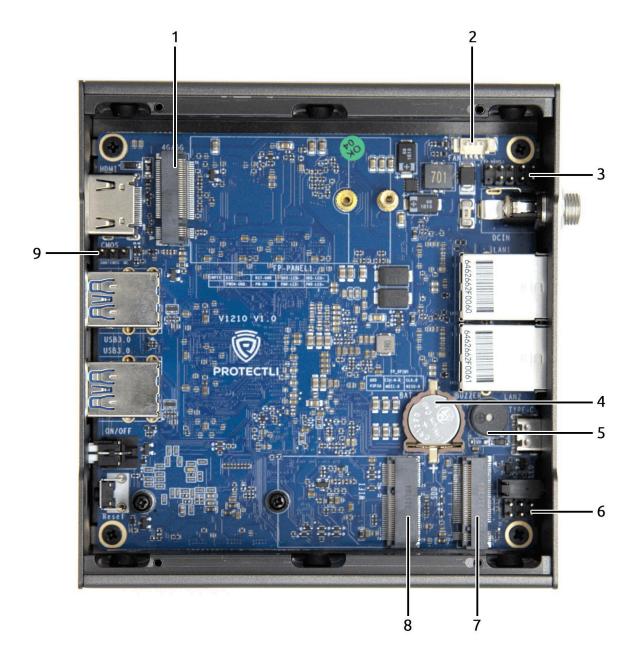
Side Features



Item #	Object	Description
1	Antenna Ports	Three antenna ports for adding radio antennas (WiFi, LTE, etc.). The ports are covered by plugs while not in use.
2	Antenna Ports	<i>(Unpictured on the reverse side.)</i> Three antenna ports for adding radio antennas (WiFi, LTE, etc.). The ports are covered by plugs while not in use.



Motherboard Top View



ltem #	Object	Label	Description
1	LTE Expansion Slot	4G/5G	M.2 3052 B-Key connector for USB 3.2 Gen 2 functionality. Designed for Protectli LTE modules, but is not limited in its capabilities.



CPU Fan Header	FAN	Four-pin PicoBlade-compatible header for an optional fan.		
Front Panel Header	FP_PANEL1	Internal header for adding external device controls and indicators featured through the front panel, such as power button, reset button, activity LEDs, etc.		
CMOS Battery	BAT	3V CR1220.		
Buzzer	BUZZER	PC Speaker.		
eSPI Header	FP_6PIN1	eSPI header for BIOS programming. Pinout is silkscreened on the motherboard, and reads as follows: • Pin 1 - V3P3A: +3.3 VDC • Pin 2 - GND: Ground • Pin 3 - MOSI-R: Main Out, Sub In • Pin 4 - CSO-N-R: Chip Select • Pin 5 - MISO-R: Main In, Sub Out • Pin 6 - CLK-R: Serial clock Pin numbering is as follows, oriented to the above image of the motherboard: Pin 2 - GND Pin 4 - CSO-N-R Pin 6 - CLK-R Pin 1 - V3P3A Pin 3 - MOSI-R Pin 5 - MISO-R		
M.2 NVMe Connector	SDD ¹	Connector uses PCIe 3.0 x1 protocol over an M.2 M-Key socket. It is designed for an NVMe storage device, but is otherwise a functional PCIe port.		
WiFi Expansion Slot	WIFI	Connector uses PCIe 3.0 x1 protocol over an M.2 Key E socket. Designed for Protectli WiFI modules, but is not limited in its capabilities.		
NVRAM Reset Jumper	CMOS	 Shorting the jumper pins GND and CMOS while the CMOS battery is connected will reset the BIOS NVRAM. Pin 1 - GND: Ground Pin 2 - CMOS: CMOS reset when grounded Pin 3 - NC: No connection Pin number is as follows, oriented to the above image of the motherboard: Pin 1 - GND Pin 2 - CMOS Pin 3 - NC		
	Header CMOS Battery Buzzer eSPI Header SPI Header M.2 NVMe Connector WiFi Expansion Slot	HeaderImage: constant set of the set of t	Front Panel HeaderFP_PANEL1Internal header for indicators featured power button, reseCMOS BatteryBAT3V CR1220.BuzzerBUZZERPC Speaker.eSPI HeaderFP_6PIN1eSPI header for BIG silkscreened on the • Pin 1 - V3P • Pin 2 - GND • Pin 5 - MIS • Pin 6 - CLKM.2 NVMe ConnectorSDD 1Connector uses PC socket. It is designed otherwise a functionWiFi Expansion SlotWIFIConnector uses PC socket. Designed f limited in its capableNVRAM Reset JumperCMOSShorting the jumper CMOSPin number is as for the motherboard Pin 3 - NC: Pin 1 - CMOSShorting the jumper	Front Panel HeaderFP_PANEL1Internal header for adding external d indicators featured through the front power button, reset button, activity LCMOS BatteryBAT3V CR1220.BuzzerBUZZERPC Speaker.eSPI HeaderFP_6PIN1eSPI header for BIOS programming. F silkscreened on the motherboard, and 0 Pin 1 - V3P3A: +3.3 VDC Pin 2 - GND: Ground Pin 3 - MOSI-R: Main Out, Sub Pin 4 - CSO-N-R: Chip Select Pin 5 - MISO-R: Main In, Sub C Pin 6 - CLK-R: Serial clockM.2 NVMe ConnectorSDD 1Connector uses PCIe 3.0 x1 protocol of socket. It is designed for an NVMe sto otherwise a functional PCIe port.WiFi Expansion SlotWIFIConnector uses PCIe 3.0 x1 protocol of socket. Designed for Protectli WiFI In limited in its capabilities.NVRAM Reset JumperCMOSShorting the jumper pins GND and CN CMOS battery is connected will reset Pin 1 - GND: Ground Im 1 - GND: Ground Pin 2 - CMOS: CMOS reset with Pin 3 - NC: No connection

¹ The silkscreen on the motherboard reads "SDD" but should instead read "SSD".



Measurement View



Document History

2024-06-28

• Clarified PCI and USB specifications such as speed, protocol, etc.

2024-05-09

• Initial document